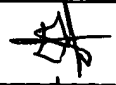
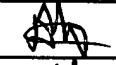



INFORMATION DISCLOSURE STATEMENT	Atty Docket:	00GR2230
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	Applicant:	DUBORGEL
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10/035033
12/26/01

U.S. PATENT DOCUMENTS							
Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date
	AA						
	AB						
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	AG						
	AH						
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	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub Class	Translation
	AM						
	AN						
OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)							
	AO	"Low Cost Overflow Generation Device for a Multiplier-Accumulator" published by IMB Technical Disclosure Bulletin, New York; Vol. 28, No. 5 dated October 1, 1985; pages 1976-1977, XP000715017					
	AP	Kai-Ping Yiu; "On Sign Bit Assignment for a Vector Multiplier"; by proceedings of the IEEE, New York; Vol. 64, No. 3 dated March 1976; pages 372-373; XP-002179148					
	AQ	Kunitoshi Aono et al.; "Image Processor with a Reconfigurable Pipeline Architecture" proceedings of the Custom Integrated Circuits Conference, San Diego, 15-18 May, 1989; New York, IEEE, US; Vol. Conf. 11 dated May 15, 1989; pages 2441-2444; XP000075588.					

EXAMINER: <u>D.H. Mulzahn</u>	DATE CONSIDERED: <u>10/19/04</u>
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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